## Attorney Docket No.: L&L-10225 Appl. No.: 10/730619 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Applicant: BURKHARD BECKER INFORMATION DISCLOSURE STATEMENT BY APPLICANT Filing Date: December 8, 2003 (37 CFR 1.98(b)) Group Art Unit: EXAMINER FILING. PATENT NO. DATE PATENTEE CLASS INITIALS CLASS DATE YC /Y.C./ Α 5.539.780 7/23/96 **Dutkiewicz** YC в 5.905.757 5/18/99 Kundmann et al. YC С 6.195.782 B1 2/27/01 Rahmatullah et D 5.537.445 7/16/96 Blaker et al. YC E F G FOREIGN PATENT DOCUMENT TRANSL. DOCUMENT NO. DATE COUNTRY CLASS CLASS YES INO YC ı. 0 899 887 42 3/3/99 Europe YC V ĸ 0 590 597 A2 4/6/94 Europe L М Ν OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Tietze, U. et al.: "Halbleiterschaltungstechnik, 11,2,2 RAM als Schieberegister". Springer Verlag, Vol. 10, 1993, pp. 284-285 and English translation "Electronic Circuits, 11.2.2 RAMs as Shift Registers", Springer Verlag, Vol. 9, 1991, pp. 284-285: DATE CONSIDERED 12/10/2006 **EXAMINER** /Yong Choe/

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